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G Stagg

J McClelland

M Koval

R Andraka

W Lawton

[BOOK] [Computer Methods in Power System Analysis](#)

GW Stagg, AH El-Abiad - 1968 - [books.google.com](#)

... 80286 481 9.15 Bus Hold and HLDA Sequence 484 9.16 Interrupt Acknowledge Sequence 485 9.17 InstructionSetFeatures 486 9.18 80287 Math Coprocessor 492 Summary ...

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[System and method for parsing and executing a single instruction stream using a plurality of tightly ...](#) - [all 2 versions »](#)

S Lacey - US Patent 5,822,570, 1998 - [Google Patents](#)

... When the microprocessor encountered an instruction from the math coprocessor instruction set, the microprocessor could pass 40 the instruction to the math ...

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[Fast Implementation of Elliptic Curve Defined over GF \(pm\) on CalmRISC with MAC2424 Coprocessor](#) - [all 6 versions »](#)

JW Chung, SG Sim, PJ Lee - [Cryptographic Hardware and Embedded Systems--CHES 2000: ...](#), 2000 - [books.google.com](#)

... GF (p) and GF (pm) were popular in computer software implementation but they were not in hardware implementation because a math coprocessor is required for its ...

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TR Hoffmann - [Management Science](#), 1992 - [JSTOR](#)

... Benchmarking of the procedure is easier since implementation is on an eight-megahertz IBM AT with a math coprocessor, using the standard IBM PC Professional ...

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[Synchronization techniques for multimedia data streams](#) - [all 3 versions »](#)

MJ Koval, WW Lawton, GA McClain, JG Tyler, SL ... - US Patent 5,333,299, 1994 - [Google Patents](#)

... 89 DATA RAM 86 MATH COPROCESSOR CABLE 94 90 ... The (BIC) 16, a math coprocessor 18, and a small computer first function is to control data streaming so as to pro- ...

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[Multimedia system having software mechanism providing standardized interfaces and controls for the ...](#) - [all 3 versions »](#)

RS Baker, RE DuLaney, PA Esack, SR Henson, SR Swix ... - US Patent 5,428,730, 1995 - [Google Patents](#)

... Page 3. US Patent June 27, 1995 Sheet 1 of 7 16 10 44 60 GRAPHICS VRAM 50 \MATH COPROCESSOR 40 ~\ WQp VOr Tt^VT A KII-1 /U VFB MI iy GO — fiR OO 1 14 OAP ...

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SM Hancock, MA Pietras, AA Rodriguez - US Patent 5,319,793, 1994 - [Google Patents](#)

... 88 49 84 INST. RAM 50 1 02 106 AIC DSP ROM EC 89 DATA RAM 86 38 MATH COPROCESSOR 46 CABLE 94 90 45 39 14 HD 41 DATA BUFFER 18 SCSI ADAPTER 24 IOC 68 44 60 ?± ...

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TW Bereiter, D Gan - US Patent 5,917,492, 1999 - [Google Patents](#)

... vendors. In addition to CPU 26, an optional math coprocessor 27, cache control-root. The "My Computer" object underlies the root. ...

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[Audio/video synchronization for application programs](#) - all 3 versions »

RL Yee - US Patent 5,471,576, 1995 - Google Patents

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PS MacIin, J Dempsey, J Brooks, J Rand - Journal of Medical Systems, 1991 - Springer

... The hardware was a generic 80386-25 micro- computer with zero wait states and a 80387 math coprocessor running MS-DOS 5.00(beta). RESULTS ...

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